09/943,888



REMARKS

Claims 1-22 are pending in the application. Claims 1-22 remain in the application without amendment.

Applicant requests reexamination and reconsideration of the application in light of the references listed in the Information Disclosure Statement filed concurrently herewith.

Claims 1, 2, 3, 8, 10-12, and 17-20 stand rejected under 35 USE 102 as being anticipated by Lee 753. Applicant respectfully traverses this rejection.

To anticipate a claim, the reference must teach every aspect of the claimed invention either explicitly or impliedly. (MPEP 706.02.) Independent claims 1 and 10 recites, among other things, "first, second, third, and fourth MOS transistor devices configured in a translinear loop, respectively.

As evident from the reference entitled "Analog VLSI Signal Processing: Why, Where and How?" at page 12, a translinear loop is understood to be, "The base-emitter junctions of an even number of transistors are connected in series, half of them in each direction "

Applicant submits that the cited reference does not teach the combination defined by independent claims 1 and 10, particularly the quoted limitation. Thus, claims 1 and 10 are patentably distinguishable over the cited reference. Accordingly, the rejection of claims 1 and 10, and claims 2, 3, 8, 11, 12, and 17-20 at least by virtue of their dependency on claims 1 and 10, under 35 USC102(b) should be withdrawn in the next Office action.

The remaining dependent claims 4-7, 9, 13-16, 21, and 22 stand rejected under 35 USC 103(a) as being unpatentable over Lee 753 in view of various patents. Applicant respectfully

010186

traverses this rejection at least by virtue of the dependency of the remaining dependent claims on claims 1 and 10, which are patentably distinguishable over Lee '753. Accordingly, the rejection of the claims under 35 USC103(a) should be withdrawn in the next Office action.

In view of the foregoing, Applicant submits that all pending claims are in condition for allowance. Applicant respectfully requests the reconsideration and reexamination of this application and the timely allowance of the pending claims. Should any issues remain unresolved, the Examiner is encouraged to telephone the undersigned at the number provided below.

If there are any other fees due in connection with the filing of the response, please charge the fees to our Deposit Account No. 17-0026. If a fee is required for an extension of time under 37 CFR 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

Dated: May 23, 2003

": ____

Donald C. Kordich Attorney for Applicant Registration No. 38,213

QUALCOMM Incorporated 5775 Morehouse Drive San Diego, California 92121-2779 Telephone: (858) 651-6399 Facsimile: (858) 658-2502